

ABSTRACT

A digital image processing apparatus for applying pixel-based color correction to an input image to generate an output image is described herein. The apparatus includes color correction logic arranged to provide two or more color correction processes. Each color correction process has a respective associated locus in a color space and a respective associated color mapping operation. The color correction processes are arranged as a succession of processes so that the results of a color correction process form the input to a next such process in the succession. Each color correction process is operable to detect whether each pixel lies within the respective locus in color space and, if so, to apply the color mapping operation to that pixel. Each color correction process after the first process in the succession is configured to inhibit color mapping in respect of the loci associated with previous processes in the succession.